AMENDMENTS TO THE SPECIFICATION

Please amend the Specification as follows:

Amend the paragraph beginning at page 3, line 19-page 4, line 5, as follows:

A method and an apparatus for allowing random insertion of certain types of computer instructions into a queue consists of assigning each program step (i.e., instruction) a unique number (INUM), issuing each program step to the appropriate execution queue (for example a load queue). The program step is assigned at a random time to a specific numbered location in the selected execution queue based upon an ascending INUM order of the program steps in the particular execution queue. A modulus value is calculated for the instruction based upon its numbered location in the particular queue, with the divisor of the modulus equal to the number of the ~~locations~~ location in the particular queue, and a status bit is set based upon the product of the modulus. A valid bit is set for the program step until the execution of the step is completed. The next program step with the same modulus that is issued is compared to the value of the previous instruction's valid bit and to the status bit. The queue is determined to be full and not capable of accepting further issued instructions based upon the compared values of the valid and status bits. The program number (INUM) and the location in the selected execution queue is recorded in a load store if the selected numbered location is empty, and a memory full flag is transmitted to the issue queue if the selected location is occupied.